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He has extensive experience in education and industry and is very active professionally in the fields of circuits and systems. He was Editor in Chief of the IEEE Transactions on Circuits and Systems Series I and II, President of the IEEE Circuits and Systems Society and is the Founding Editor and Editor in Chief of the Journal of Circuits, Systems, and Computers. He is the recipient of the Golden Jubilee Medal, the Education Award, and the Meritorious Service Award from the IEEE Circuits and Systems Society and the Third Millennium Medal from the IEEE. Professor Chen is a fellow of the IEEE and the American Association for the Advancement of Science. 77 chapters encompass the entire field of electrical engineering. THOUSANDS of valuable figures, tables, formulas, and definitions. Extensive bibliographic references. **National Association of**

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Analysis and Design of Analog Integrated Circuits Paul R. Gray, Paul J. Hurst, Stephen H. Lewis, Robert G. Meyer, 2024-01-31. ANALYSIS AND DESIGN OF ANALOG INTEGRATED CIRCUITS. Authoritative and comprehensive textbook on the fundamentals of analog integrated circuits with learning aids included throughout. Written in an accessible style to ensure complex content can be appreciated by both students and professionals. This Sixth Edition of Analysis and Design of Analog Integrated Circuits is a highly comprehensive textbook on analog design offering in-depth coverage of the fundamentals of circuits in a single volume. To aid in reader comprehension and retention, supplementary material includes end-of-chapter problems plus a Solution Manual for instructors. In addition to the well-established concepts, this Sixth Edition introduces a new super source follower circuit and its large signal behavior, frequency response, stability, and noise properties. New material also introduces replica biasing, describes and analyzes two op amps with replica biasing, and provides coverage of weighted zero value time constants as a method to estimate the location of dominant zeros, pole-zero doublets, including their effect on settling time, and three examples of circuits that create doublets, the effect of feedback on pole-zero doublets.

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