

CHAPTER 1 INTRODUCTION

- 1.1 An ADD/SUBTRACT logic circuit is shown below. It performs the ADD operation for $P = 0$, and SUBTRACT for $P = 1$.
- Draw an equivalent CMOS logic diagram by noting that most CMOS gates, except for transmission gate and XOR, are inverting. For example, AND gate is implemented with NAND followed by an inverter.
 - By using the gate array platform given on page 47, implement the CMOS circuit as compactly as possible with the aspect ratio, which is the ratio of vertical dimension to horizontal dimension, as close to 1 as possible.

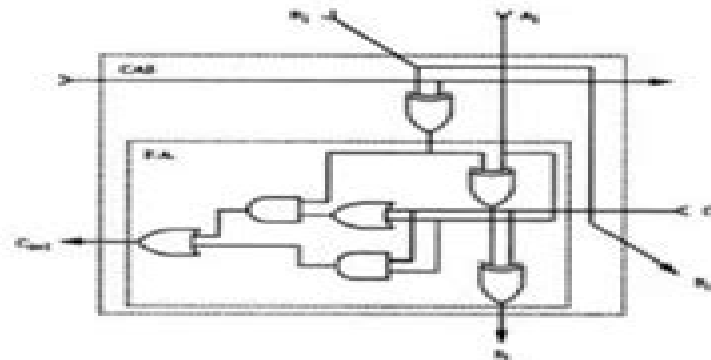


Figure P14.1(a)

SOLUTION:

- The CMOS circuit implementation using the gate array platform:

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Thomas A. DeMassa, Zack Ciccone



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Electronic Devices and Circuit Fundamentals, Solution Manual Dale R Patrick, Stephen W. Fardo, Ray E. Richardson, Vigyan (Vigs) Chandra, 2023-05-26 Devices and Circuit Fundamentals is Chapter Outline Learning Objectives Key Terms Figure List Chapter Summary Formulas Answers to Examples Self Exams Glossary of Terms defined Solution Manual to Accompany Analysis and Design of Integrated Electronic Circuits, 1987 *Digital Integrated Circuits* Jan M. Rabaey, 2003 *Digital integrated Circuits*, 1976 *Solutions Manual to Accompany Digital Concepts Using Standard Integrated Circuits* Richard S. Sandige, 1978 **Solution Manual Digital Logic** Jefferson C. Boyce, 1982 **Digital Integrated Circuits** Jan M. Rabaey, Anantha P. Chandrakasan, Borivoje Nikolić, 2008 *Digital Integrated Circuits* John E. Ayers, 2018-09-03 Exponential improvement in functionality and performance of digital integrated circuits has revolutionized the way we live and work The continued scaling down of MOS transistors has broadened the scope of use for circuit technology to the point that texts on the topic are generally lacking after a few years The second edition of *Digital Integrated Circuits Analysis and Design* focuses on timeless principles with a modern interdisciplinary view that will serve integrated circuits engineers from all disciplines for years to come Providing a revised instructional reference for engineers involved with Very Large Scale Integrated Circuit design and fabrication this book delves into the dramatic advances in the field including new applications and changes in the physics of operation made possible by relentless miniaturization This book was conceived in the versatile spirit of the field to bridge a void that had existed between books on transistor electronics and those covering VLSI design and fabrication as a separate topic Like the first edition this volume is a crucial link for integrated circuit engineers and those studying the field supplying the cross disciplinary connections they require for guidance in more advanced work For pedagogical reasons the author uses SPICE level 1 computer simulation models but

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Digital Integrated Circuits Thomas A. DeMassa, Zack Ciccone, 1996. Contains the most extensive coverage of digital integrated circuits available in a single source. Provides complete qualitative descriptions of circuit operation followed by in-depth analytical analyses and spice simulations. The circuit families described in detail are transistor logic, TTL, STTL, and ASTTL, emitter coupled logic, ECL, NMOS logic, CMOS logic, dynamic CMOS, BiCMOS structures, and various GaAsFET technologies. In addition to detailed presentation of the basic inverter circuits for each digital logic family, complete details of other logic circuits for these families are presented. *CMOS Digital Integrated Circuits* Sung-Mo Kang, Yusuf Leblebici, 2002. The fourth edition of CMOS Digital Integrated Circuits Analysis and Design continues the well-established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design as well as addressing state-of-the-art technology issues highlighted by the widespread use of nanometer-scale CMOS technologies. In this latest edition, virtually all chapters have been rewritten; the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations, and the material has been reinforced with up-to-date examples. The broad-ranging coverage of this textbook starts with the fundamentals of CMOS process technology and continues with MOS transistor models, basic CMOS gates, interconnect effects, dynamic circuits, memory circuits, arithmetic building blocks, clock and I/O circuits, low-power design techniques, design for manufacturability, and design for testability. Digital Integrated Circuits, 1983. Digital Integrated Circuits, Manual for Integrated Circuit Users John D. Lenk, 1973.

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