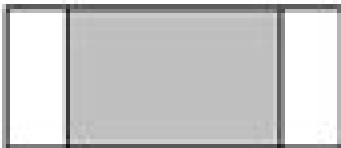
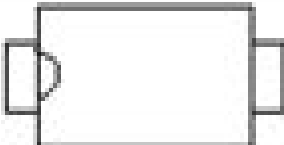
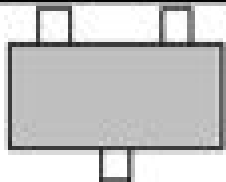
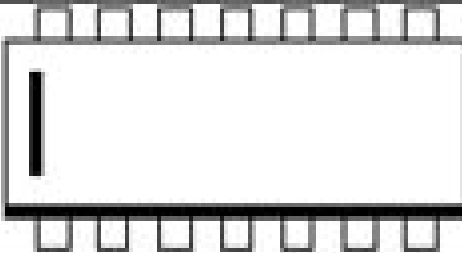


Component	Shape	Markings
Chip resistor		Labeled with value (see Table 3)
Chip capacitor		Not marked
Polarized capacitor		Plus end marked with band, value marked
Diode		Cathode end marked with notch or band
SOT (Small Outline Transistor)		May be marked, unmarked, or house numbered
SOIC (Small Outline Integrated Circuit)		May be marked, unmarked, or house numbered. Pin one marked with beveled side, dot, band, or notch

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Emilie Sanchez



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Mount Technology for Concurrent Engineering and Manufacturing Frank Classon, 1993 A guide to gaining the valuable miniaturization and cost saving benefits of surface mount technology SMT showing how to integrate multiple

company functions designs manufacturing testing and management and save time and money at every stage The Electronics Assembly Handbook Frank Riley, Electronic Packaging and Production, 2013-06-29 The assembly of electronic circuit boards has emerged as one of the most significant growth areas for robotics and automated assembly This comprehensive volume which is an edited collection of material mostly published in Assembly Engineering and Electronic Packaging and Production will provide an essential reference for engineers working in this field including material on Multi Layer Boards Chip on board and numerous case studies Frank J Riley is senior vice president of the Bodine Corporation and a world authority on assembly automation

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Printed Circuit Boards R. S. Khandpur,2005-09-07 The printed circuit is the basic building block of the electronics hardware industry This is a comprehensive single volume self teaching guide to the art of printed circuit board design and fabrication covering the complete cycle of PCB creation design layout fabrication assembly and testing

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Fine Pitch Surface Mount Technology Phil Marcoux, 2013-11-27 Fine pitch high lead count integrated circuit packages represent a dramatic change from the conventional methods of assembling electronic components to a printed interconnect circuit board To some these FPT packages appear to be an extension of the assembly technology called surface mount or SMT Many of us who have spent a significant amount of time developing the process and design techniques for these fine pitch packages have concluded that these techniques go beyond those commonly used for SMT In 1987 the present author convinced of the uniqueness of the assembly and design demands of these packages chaired a joint committee where the members agreed to use fine pitch technology FPT as the defining term for these demands The committee was unique in several ways one being that it was the first time three U S standards organizations the IPC Lincolnwood IL the EIA Washington D C and the ASTM Philadelphia came together to create standards before a technology was in high demand The term fine pitch technology and its acronym FPT have since become widely accepted in the electronics industry The knowledge of the terms and demands of FPT currently exceed the usage of FPT packaged components but this is changing rapidly because of the size performance and cost savings of FPT I have resisted several past invitations to write other

technical texts However I feel there are important advantages and significant difficulties to be encountered with FPT

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