

The background of the cover is a high-magnification, false-color micrograph of a semiconductor die. The die is rectangular and filled with a complex, repeating pattern of circuitry. The colors are primarily red, green, and blue, with some yellow and purple highlights, creating a vibrant, grid-like appearance. The perspective is slightly angled, giving a three-dimensional feel to the flat surface.

FOURTH EDITION

CMOS VLSI DESIGN

**A CIRCUITS
AND
SYSTEMS
PERSPECTIVE**

NEIL H. E. WESTE

DAVID MONEY HARRIS

Solution Manual Cmos Vlsi Design 4th Edition

Shasha Hu



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