

Digital System Design

Lecture 11

Combinational Logic Design

Objectives:

1. Design procedure.
2. Fundamental circuits.

1. Design procedure

- Design procedure has five steps:
 - *Specification.*
 - *Formulation.*
 - *Optimization.*
 - *Technology mapping.*
 - *Verification.*
- **Specification:**
The design of a combinational circuit starts with the specification of the problem:
Write a specification for the given circuit (text or HDL description (hardware description language)) with symbols for inputs and outputs.
- **Formulation:**
Derive the truth table or initial Boolean expressions (that define the required relationships between inputs and outputs).
Formulation converts the specification into forms that can be optimized (truth table or Boolean expression).
- **Optimization:**
 - Any available methods to minimize the logic:
 - ✓ **Algebraic manipulation.**
 - ✓ **K-map method.**
 - ✓ **Computer-based program.**
 - Then, we can use:**
 - ✓ **Two-level optimization or multiple-level optimization to get less cost (use NAND and NOR gate technologies).**
- **Technology mapping (implementation):**
Transform the logic diagram to new logic diagram with available implementation.
- **Verification:**
Verify the correctness of the final design.

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Tocci

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Digital Design William James Dally,R. Curtis Harting,2012-09-17 This book provides students with a system level perspective and the tools they need to understand analyze and design complete digital systems using Verilog It goes beyond the design of simple combinational and sequential modules to show how such modules are used to build complete systems reflecting digital design in the real world **Digital Design with RTL Design, VHDL, and Verilog** Frank

Vahid,2010-03-09 An eagerly anticipated up to date guide to essential digital design fundamentals Offering a modern updated approach to digital design this much needed book reviews basic design fundamentals before diving into specific details of design optimization You begin with an examination of the low levels of design noting a clear distinction between design and gate level minimization The author then progresses to the key uses of digital design today and how it is used to build high performance alternatives to software Offers a fresh up to date approach to digital design whereas most literature available is sorely outdated Progresses through low levels of design making a clear distinction between design and gate level minimization Addresses the various uses of digital design today Enables you to gain a clearer understanding of applying digital design to your life With this book by your side you ll gain a better understanding of how to apply the material in the book to real world scenarios Algorithms for VLSI Design Automation Sabih H. Gerez,1999-01-05 Modern microprocessors

such as Intel s Pentium chip typically contain many millions of transistors They are known generically as Very Large Scale Integrated VLSI systems and their sheer scale and complexity has necessitated the development of CAD tools to automate their design This book focuses on the algorithms which are the building blocks of the design automation software which generates the layout of VLSI circuits Courses on this area are typically elective courses taken at senior undergrad or graduate level by students of Electrical and Electronic Engineering and sometimes in Computer Science or Computer Engineering *Digital Integrated Circuit Design* Hubert Kaeslin,2008-04-28 This practical tool independent guide to designing digital circuits takes a unique top down approach reflecting the nature of the design process in industry Starting with architecture design the book comprehensively explains the why and how of digital circuit design using the physics designers need to know and no more **Digital Design Using VHDL** William J. Dally,R. Curtis Harting,Tor M.

Aamodt,2016 Provides students with a system level perspective and the tools they need to understand analyze and design complete digital systems using VHDL It goes beyond the design of simple combinational and sequential modules to show how such modules are used to build complete systems reflecting digital design in the real world *Special Libraries* ,1975 Most vols include Proceedings of the Special Libraries Association *Journal of Design Automation & Fault-tolerant Computing* ,1976 **Computer Simulated Experiments for Digital Electronics Using Electronics Workbench** Richard H. Berube,1999 COMBINATIONAL LOGIC CIRCUITS 1 Preliminary Concepts 2 Logic Gates INVERTER OR and AND 3 Logic Gates NAND and NOR 4 Boolean Theorems 5 Universality of NAND and NOR Gates 6 Analyzing Combinational Logic Circuits

7 Simplifying Combinational Logic Circuits 8 Logic Simplification Using Karnaugh Maps 9 Designing Combinational Logic Circuits 10 Troubleshooting Combinational Logic Circuits II ARITHMETIC LOGIC CIRCUITS 11 Logic Gates XOR and XNOR 12 Arithmetic Circuits 13 Parallel Binary Adder 14 BCD Adder 15 Parity Generator Checker 16 Magnitude Comparator 17 Troubleshooting Arithmetic Circuits III MSI LOGIC CIRCUITS 18 Decoders and Encoders 19 Multiplexers and Demultiplexers 20 Troubleshooting MSI Logic Circuits IV SEQUENTIAL LOGIC CIRCUITS 21 S R and D Latches 22 Edge triggered Flip Flops 23 Monostable and Astable Multivibrators 24 Registers and Data Storage 25 Asynchronous Counters 26 Synchronous Counters 27 BCD Counters 28 Troubleshooting Sequential Logic Circuits V INTERFACING THE ANALOG WORLD 29 Digital to Analog Converters 30 Analog to Digital Converters 31 Data Acquisition Appendix A IC Chip Pin Diagrams Appendix B Notes on Using Electronics Workbench Bibliography Engineering Education ,1975 Computer Simulated Experiments for Digital Electronics Using Electronics Workbench Multisim Richard H. Berube,2004-09 This unique and innovative laboratory manual helps users learn and understand circuit analysis concepts by using Electronic Workbench software to simulate actual laboratory experiments on a computer Learners work with circuits drawn on the computer screen and with simulated instruments that act like actual laboratory instruments Circuits can be modified easily with on screen editing and analysis results provide fast accurate feedback Hands on in approach throughout in both interactive experiments and a series of questions about the results of each experiment it is more cost effective safer and more thorough and efficient than using hardwired experiments This For use with any DC AC text Logic Design Projects Using Standard Integrated Circuits John F. Wakerly,1976 **VLSI Systems Design** ,1986 **Fundamentals of Logic Design and Switching Theory** Arthur D. Friedman,1986 *Sm Digital Systems L/M Results* Tocci,1997-08 *Microprocessor and Microcontroller Fundamentals* William Kleitz,1998 Short concise and easily accessible this book uses the 8085A microprocessor and 8051 microcontroller to explain the fundamentals of microprocessor architecture programming and hardware It features only practical workable designs so that readers can develop a complete understanding of the application with no frustrating gaps in the explanations An abundance of real life hardware software and schematic interpretation problems prepare readers to troubleshoot and trace signals through situations they will likely encounter on the job *Books in Print* ,1994 Computer Logic Design M. Morris Mano,1972 **Theory & Design of Switching Circuits** Arthur D. Friedman,Premachandran R. Menon,1975 1 ex uitleenbaar 1 ex niet uitleenbaar **The Elements of Inductive Logic, Designed Mainly for the Use of Students in the Universities** Thomas Fowler,1876 **Troubleshooting Digital Systems** Jim C. Deloach,Deloach,Ambrosio,1995

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