

**Previous VTU Question Papers with
Suggested Scheme of Answers/ Solutions**

SUBJECT: Computer Techniques in Power Systems

EE72

VTU QUESTION PAPER of Dec.06 / Jan.07

- Note:**
1. Answer any five questions.
 2. Assume missing data suitably.

1.
 - a. Explain the significance of primitive network and hence get the performance equations in both impedance and admittance form. (5 Marks)
 - b. Derive an expression for obtaining the bus admittance matrix using singular transformations. (7 Marks)
 - c. Determine the bus admittance matrix using the singular transformations for the sample power system with the line data shown in table below. (8 Marks)

Line No.	1	2	3	4	5
Bus-code p-q	0 - 1	1 - 2	2 - 3	3 - 0	2 - 0
Impedance pu)	0.8	0.5	0.4	0.5	0.25

2.
 - a. Obtain the generalized algorithmic expression for bus impedance matrix elements when a link is added to the partial network. Also discuss the special cases. (14 Marks)
 - b. Explain the steps to modify the Z_{BUS} for removal of a line or modification of the line impedance. (6 Marks)
3.
 - a. What are different types of buses considered during power system load flow analysis? Explain briefly. (5 Marks)
 - b. Determine the voltages at the end of first iteration using GS method for the system data given below. Assume an acceleration factor of 1.6. (15 marks)

i) Line Data

Bus Code	1 - 2	1 - 3	2 - 3	2 - 4	3 - 4
Admittance	$2 - j 8$	$1 - j 4$	$0.66 - j 2.664$	$1 - j 4$	$2 - j 8$

ii) Bus Data

Bus No.	P	Q	V	Remarks
1	-	-	$1.06 \angle 0$	SLACK
2	0.5	0.2	$1 + j 0$	PQ
3	0.4	0.3	$1 + j 0$	PQ
4	0.3	0.1	$1 + j 0$	PQ

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