

Figure 24. Typical Modulator Circuit

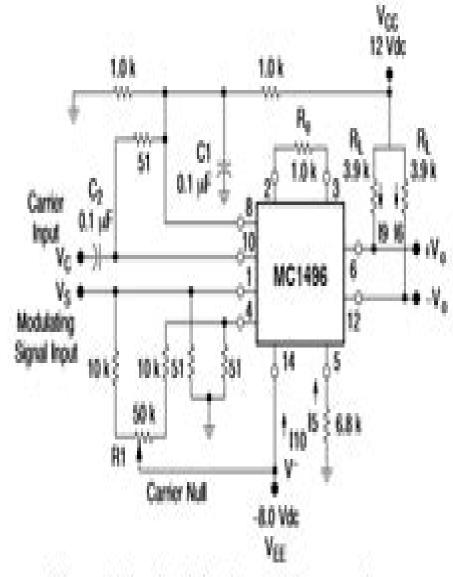


Figure 5. Carrier Rejection and Suppression

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