

Routing Requirements Overview

	Who Selects Carrier?	Pick Up Responsibilities	Delivery Responsibilities
TL Collect	Lowe's	Carrier will schedule pickup with Vendor	Carrier contacts Lowe's DC or Stores 24 hrs prior to delivery for appointment
		Vendors to notify Lowe's if appointment is NOT made by noon the day before the scheduled ship date	Carrier may drop trailer at DC if approved for drop trailer program
TL Prepaid	Lowes's	Vendor must use core TL carrier for all RDC shipments	Carrier contacts Lowe's DC o stores 24 hrs prior to delivery for appointment
		Carrier pickups to be scheduled by vendor for scheduled ship date	Carrier may drop trailer at DC if approved for drop trailer program
LTL Collect	Lowe's	Vendor must use core LTL carrier	Core Carriers have standing appointments at DC and stores
		Vendor schedules pickup for ship date on the PO	
		Vendors to notify Lowe's if appointment is NOT made by noon the day before the scheduled ship date	
LTL Prepaid	Lowe's	Vendor schedules pickup for ship date on the PO	Core Carriers have standing appointments at DC and stores
		Vendor must use core LTL carrier	

Target Vendor Routing Guide

Vivek Kale

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customer Transportation is moving from a cost center towards a profit center The traditional logistics service providers are perceived to not innovate fast enough Top management must understand the transportation management basics and use it in their strategic decision making They should be involved in discussions on how to organize the transport management function in the best way and how to use it as a service differentiator Transportation is more than the efficient movement of supplies sub assemblies and final products In addition it is more than the key performance indicators on the business balanced scorecard Transportation management professionals fail to catch top management s attention due to the use of technical language It is more difficult to understand transportation key performance indicators such as loading degree net and gross pick up and delivery reliability It is easier to get top management attention when talking about lost sales due to stock outs lost tenders due to long delivery times high inventory holding and scrap costs The Student's Guide to VHDL Peter J. Ashenden, 2008-05-19 The Student's Guide to VHDL is a condensed edition of The Designer's Guide to VHDL the most widely used textbook on VHDL for digital system modeling The Student's Guide is targeted as a supplemental reference book for computer organization and digital design courses Since publication of the first edition of The Student's Guide the IEEE VHDL and related standards have been revised The Designer's Guide has been revised to reflect the changes so it is appropriate that The Student's Guide also be revised In The Student's Guide to VHDL 2nd Edition we have included a design case study illustrating an FPGA based design flow The aim is to show how VHDL modeling fits into a design flow starting from high level design and proceeding through detailed design and verification synthesis FPGA place and route and final timing verification Inclusion of the case study helps to better serve the educational market Currently most college courses do not formally address the details of design flow Students may be given informal guidance on how to proceed with lab projects In many cases it is left to students to work it out for themselves The case study in The Student's Guide provides a reference design flow that can be adapted to a variety of lab projects Guide to FPGA Implementation of Arithmetic Functions Jean-Pierre Deschamps, Gustavo D. Sutter, Enrique Cantó, 2012-04-02 This book is designed both for FPGA users interested in developing new specific components generally for reducing execution times and IP core designers interested in extending their catalog of specific components The main focus is circuit synthesis and the discussion shows for example how a given algorithm executing some complex function can be translated to a synthesizable circuit description as well as which are the best choices the designer can make to reduce the circuit cost latency or power consumption This is not a book on algorithms It is a book that shows how to translate efficiently an algorithm to a circuit using techniques such as parallelism pipeline loop unrolling and others Numerous examples of FPGA implementation are described throughout this book and the circuits are modeled in VHDL Complete and synthesizable source files are available for download MCTS: Windows Server 2008 **Applications Infrastructure Configuration Study Guide** Joel Stidley, 2012-05-14 This comprehensive guide offers 100 percent coverage of the exam's objectives real world scenarios hands on exercises and challenging review questions

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