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SEMICONDUCTOR PHYSICS AND DEVICES

Basic Principles

Donald A. Neamen

Fourth Edition

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Physics of Semiconductor Devices Simon M. Sze, Kwok K. Ng, 2006-12-13 The Third Edition of the standard textbook and reference in the field of semiconductor devices This classic book has set the standard for advanced study and reference in the semiconductor device field Now completely updated and reorganized to reflect the tremendous advances in device concepts and performance this Third Edition remains the most detailed and exhaustive single source of information on the most important semiconductor devices It gives readers immediate access to detailed descriptions of the underlying physics and performance characteristics of all major bipolar field effect microwave photonic and sensor devices Designed for graduate textbook adoptions and reference needs this new edition includes A complete update of the latest developments New devices such as three dimensional MOSFETs MODFETs resonant tunneling diodes semiconductor sensors quantum cascade lasers single electron transistors real space transfer devices and more Materials completely reorganized Problem sets at the end of each chapter All figures reproduced at the highest quality Physics of Semiconductor Devices Third Edition offers engineers research scientists faculty and students a practical basis for understanding the most important devices in use today and for evaluating future device performance and limitations A Solutions Manual is available from the editorial department

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Advanced High Voltage Power Device Concepts B. Jayant Baliga, 2011-09-21 The devices described in *Advanced MOS Gated Thyristor Concepts* are utilized in microelectronics

production equipment in power transmission equipment and for very high power motor control in electric trains steel mills etc Advanced concepts that enable improving the performance of power thyristors are discussed here along with devices with blocking voltage capabilities of 5 000 V 10 000 V and 15 000 V Throughout the book analytical models are generated to allow a simple analysis of the structures and to obtain insight into the underlying physics The results of two dimensional simulations are provided to corroborate the analytical models and give greater insight into the device operation *Manual of Classification* United States. Patent and Trademark Office,1996 Includes list of replacement pages *Semiconductor Device Physics and Simulation* J.S. Yuan,Juin Jei Liou,1998-05-31 The advent of the microelectronics technology has made ever increasing numbers of small devices on a same chip The rapid emergence of ultra large scaled integrated ULSI technology has moved device dimension into the sub quarter micron regime and put more than 10 million transistors on a single chip While traditional closed form analytical models furnish useful intuition into how semiconductor devices behave they no longer provide consistently accurate results for all modes of operation of these very small devices The reason is that in such devices various physical mechanisms affect the device performance in a complex manner and the conventional assumptions i e one dimensional treatment low level injection quasi static approximation etc employed in developing analytical models become questionable Thus the use of numerical device simulation becomes important in device modeling Researchers and engineers will rely even more on device simulation for device design and analysis in the future This book provides comprehensive coverage of device simulation and analysis for various modem semiconductor devices It will serve as a reference for researchers engineers and students who require in depth up to date information and understanding of semiconductor device physics and characteristics The materials of the book are limited to conventional and mainstream semiconductor devices photonic devices such as light emitting and laser diodes are not included nor does the book cover device modeling device fabrication and circuit applications

Computer-Aided Design and VLSI Device Development
 Kit Man Cham,Soo-Young Oh,John L. Moll,2013-12-19 This book is concerned with the use of Computer Aided Design CAD in the device and process development of Very Large Scale Integrated Circuits VLSI The emphasis is in Metal Oxide Semiconductor MOS technology State of the art device and process development are presented This book is intended as a reference for engineers involved in VLSI develop ment who have to solve many device and process problems CAD specialists will also find this book useful since it discusses the organization of the simula tion system and also presents many case studies where the user applies the CAD tools in different situations This book is also intended as a text or reference for graduate students in the field of integrated circuit fabrication Major areas of device physics and processing are described and illustrated with Simulations The material in this book is a result of several years of work on the implemen tation of the simulation system the refinement of physical models in the simulation programs and the application of the programs to many cases of device developments The text began as publications in journals and con ference proceedings as weil as lecture notes

for a Hewlett Packard internal CAD course This book consists of two parts It begins with an overview of the status of CAD in VLSI which points out why CAD is essential in VLSI development Part A presents the organization of the two dimensional simulation system **Fundamentals of Solid-state Electronics** Chih-Tang Sah, 1996 This Solution Manual a companion volume of the book Fundamentals of Solid State Electronics provides the solutions to selected problems listed in the book Most of the solutions are for the selected problems that had been assigned to the engineering undergraduate students who were taking an introductory device core course using this book This Solution Manual also contains an extensive appendix which illustrates the application of the fundamentals to solutions of state of the art transistor reliability problems which have been taught to advanced undergraduate and graduate students *Semiconductor Measurement Technology* United States. National Bureau of Standards, 1990 Semiconductor Measurement Technology National Institute of Standards and Technology (U.S.), 1990

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